

Description

These dual N Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

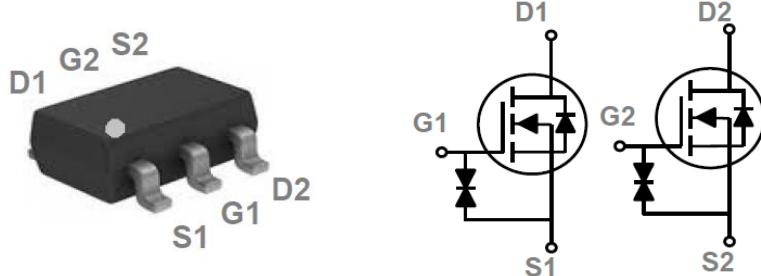
Features

- ◆ 20V/0.8A, $R_{DS(ON)} = 300\text{m}\Omega$ @ $V_{GS} = 4.5\text{V}$
- ◆ Suit for 1.5V Gate Drive Applications
- ◆ Fast switching
- ◆ G-S ESD protection diode embedded
- ◆ SOT-363 package design

Applications

- ◆ Networking
- ◆ Notebook
- ◆ Load Switch
- ◆ Hand - held Instruments

SOT-363 Dual Pin Configuration



Absolute Maximum Ratings ($T_C = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Maximum	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 8	V
Continuous Drain Current <small>$T_C = 25^\circ\text{C}$</small>	I_D	0.8	A
		0.51	
Pulsed Drain Current	I_{DM}	3.2	A
Power Dissipation <small>$T_A = 25^\circ\text{C}$</small>	P_D	275	mW
		2.2	$\text{mW}/^\circ\text{C}$
Operating junction temperature range	T_J	- 55 to 150	$^\circ\text{C}$
Storage temperature range	T_{STG}	- 55 to 150	$^\circ\text{C}$

Thermal Resistance Ratings

Parameter	Symbol	Maximum	Unit
Junction-to-Ambient	$R_{\theta JA}$	450	$^\circ\text{C}/\text{W}$



20 V Dual N-Channel MOSFETs

ECDEV2220Z

Ordering Information

Device	Package	REMARK
ECDEV2220Z	SOT-363	3000 pcs / Reel

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}} = V_{\text{DS}}, I_D = 250\mu\text{A}$	0.3	0.6	1	V
Gate Leakage Current	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 6\text{ V}$	-	-	± 20	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	μA
		$V_{\text{DS}} = 16\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$	-	-	10	
Drain-Source On Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 0.5\text{A}$	-	200	300	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 0.4\text{A}$	-	235	400	
		$V_{\text{GS}} = 1.8\text{V}, I_D = 0.2\text{A}$	-	295	550	
		$V_{\text{GS}} = 1.5\text{V}, I_D = 0.1\text{A}$	-	365	800	
		$V_{\text{GS}} = 1.2\text{V}, I_D = 0.1\text{A}$	-	600	1500	
Diode Forward Voltage	V_{SD}	$I_S = 0.2\text{A}, V_{\text{GS}} = 0\text{V}$	-	-	1	V
Dynamic Parameters						
Input Cap.	C_{iss}	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, F = 1\text{MHz}$	-	38.2	75	pF
Output Cap.	C_{oss}		-	14.4	28	
Reverse Transfer Cap.	C_{rss}		-	6	12	
Total Gate Charge	Q_g	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, I_D = 0.5\text{A}$	-	1	2	nC
Gate-Source Charge	Q_{gs}		-	0.26	0.5	
Gate-Drain Charge	Q_{gd}		-	0.2	0.4	
Turn-On Time	$T_{\text{D}(\text{ON})}$	$V_{\text{DS}} = 10\text{V}, I_D = 0.5\text{A}, V_{\text{GS}} = 4.5\text{V}, R_G = 10\Omega$	-	5	10	nS
	t_r		-	3.5	7	
Turn-Off Time	$T_{\text{D}(\text{OFF})}$		-	14	28	
	t_f		-	6	12	

Typical Characteristics

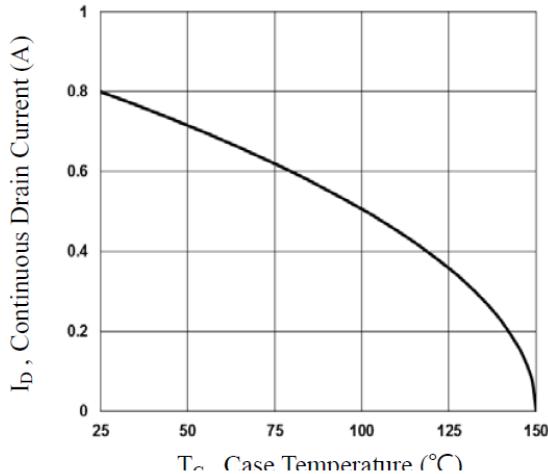


Fig.1 Continuous Drain Current vs. T_c

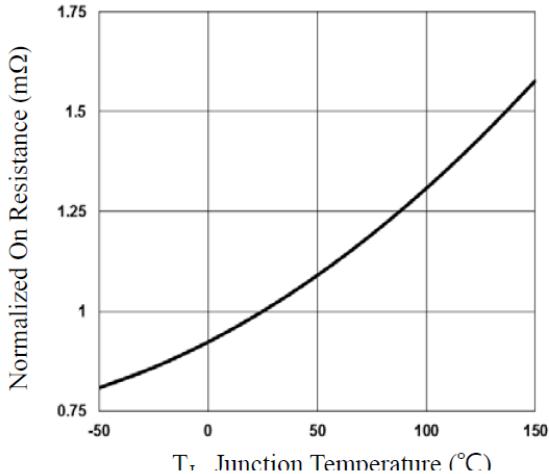


Fig.2 Normalized R_{DS(on)} vs. T_j

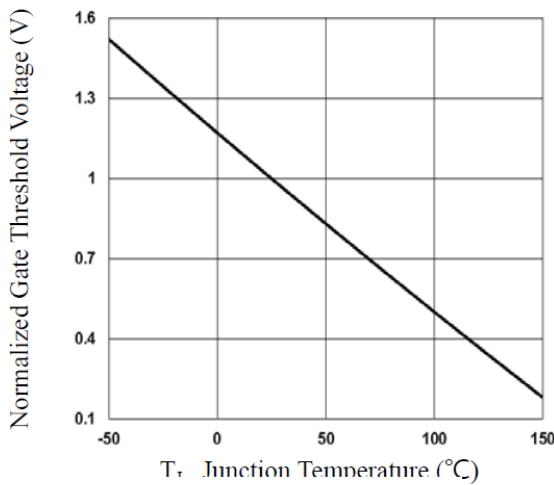


Fig.3 Normalized V_{th} vs. T_j

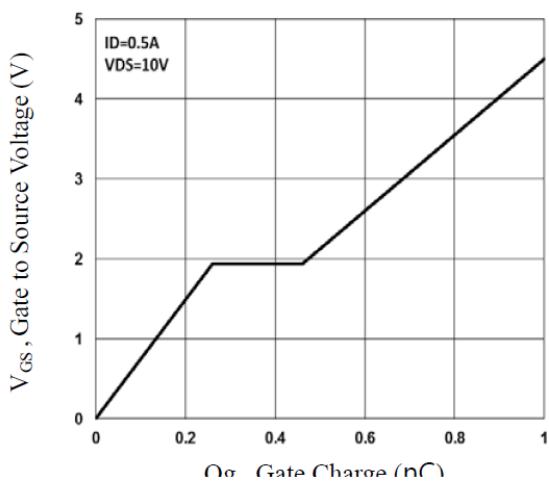


Fig.4 Gate Charge Waveform

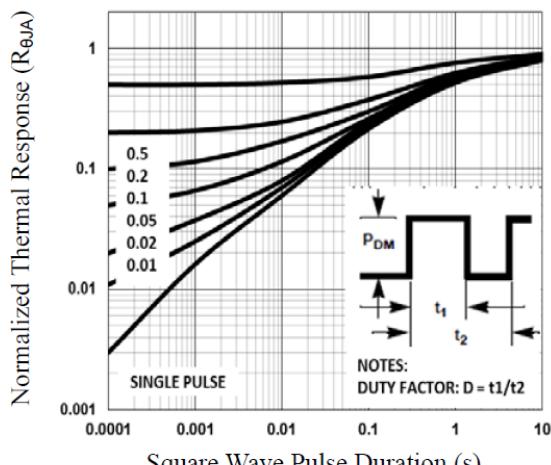


Fig.5 Normalized Transient Impedance

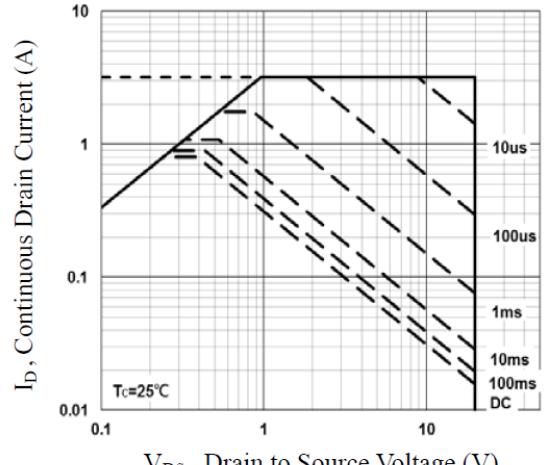
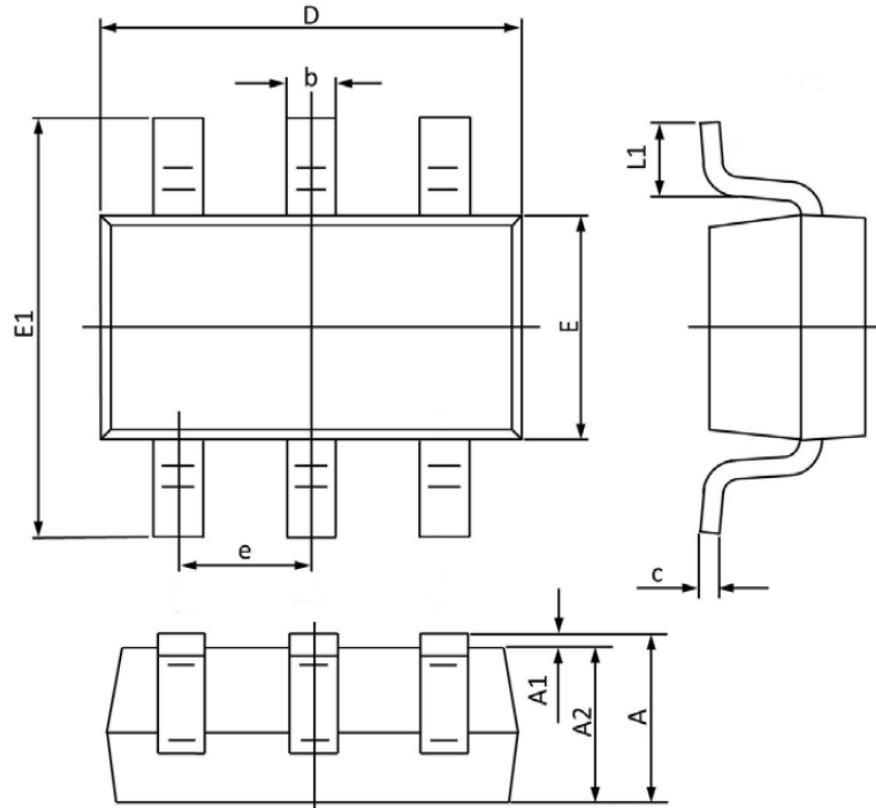


Fig.6 Maximum Safe Operation Area

Physical Dimensions

6Pin surface Mount SOT-363



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.100	0.800	0.043	0.031
A1	0.100	0.000	0.004	0.000
A2	1.000	0.800	0.039	0.031
b	0.330	0.100	0.013	0.004
c	0.250	0.100	0.010	0.004
D	2.200	1.800	0.087	0.071
E	1.350	1.150	0.053	0.045
E1	2.400	1.800	0.094	0.071
e	0.65BSC		0.026BSC	
L1	0.350	0.100	0.014	0.004