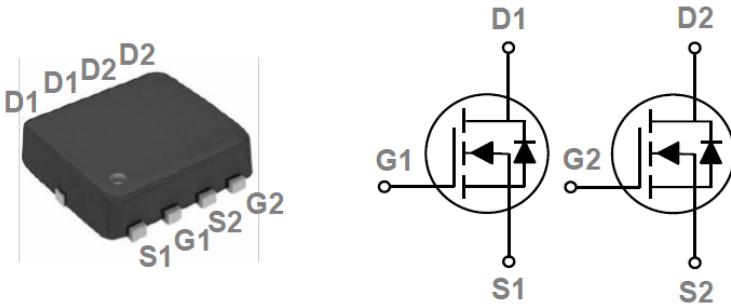


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

DFN3*3 Pin Configuration



BVDSS	RDSON	ID
30V	20mΩ	20A

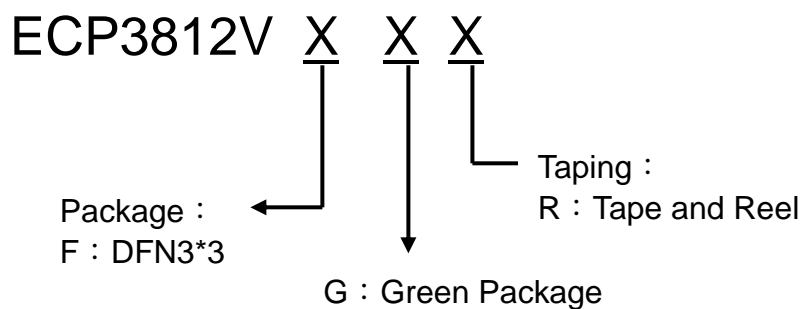
Features

- ◆ 30V, 20A, $R_{DS(ON)} = 20m\Omega @ V_{GS} = 10V$
- ◆ Improved dv/dt capability
- ◆ Fast switching
- ◆ 100% EAS Guaranteed
- ◆ Green Device Available

Application

- ◆ MB / VGA / Vcore
- ◆ POL Applications
- ◆ SMPS 2nd SR

Ordering Information





Absolute Maximum Ratings $T_C=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_C=25^{\circ}\text{C}$)	20	A
	Drain Current – Continuous ($T_C=100^{\circ}\text{C}$)	13	A
I_{DM}	Drain Current – Pulsed ¹	80	A
EAS	Single Pulse Avalanche Energy ²	14	mJ
IAS	Single Pulse Avalanche Current ²	17	A
P_D	Power Dissipation ($T_C=25^{\circ}\text{C}$)	20	W
	Power Dissipation – Derate above 25°C	0.16	W/ $^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	6.4	$^{\circ}\text{C/W}$

Electrical Characteristics $T_J=25^{\circ}\text{C}$, unless otherwise noted

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.04	---	V/ $^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=30V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=24V, V_{GS}=0V, T_J=125^{\circ}\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance ³	$V_{GS}=10V, I_D=10A$	---	17	20	$\text{m}\Omega$
		$V_{GS}=4.5V, I_D=6A$	---	23	30	$\text{m}\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	1.2	1.5	2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4	---	mV/ $^{\circ}\text{C}$
gfs	Forward Transconductance	$V_{DS}=5V, I_D=6A$	---	13	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=15V, V_{GS}=4.5V, I_D=8A$	---	4.1	6	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	1	1.4	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	2.1	4	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=15V, V_{GS}=10V, R_G=6\Omega, I_D=1A$	---	2.8	5	ns
T_r	Rise Time ^{3, 4}		---	7.2	14	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	15.8	30	
T_f	Fall Time ^{3, 4}		---	4.6	9	

C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, F=1MHz$	---	345	500	pF
C_{oss}	Output Capacitance		---	55	80	
C_{rss}	Reverse Transfer Capacitance		---	32	55	
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	3.2	6.4	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	20	A
I_{SM}	Pulsed Source Current ³		---	---	80	A
V_{SD}	Diode Forward Voltage ³	$V_{GS}=0V, I_S=1A, T_J=25^\circ C$	---	---	1	V
t_{rr}	Reverse Recovery Time	$V_{GS}=0V, I_S=1A, di/dt=100A/\mu s$ $T_J=25^\circ C$	---	8.37	---	ns
Q_{rr}	Reverse Recovery Charge		---	2.08	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=17A., R_G=25\Omega, \text{Starting } T_J=25^\circ C.$
3. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

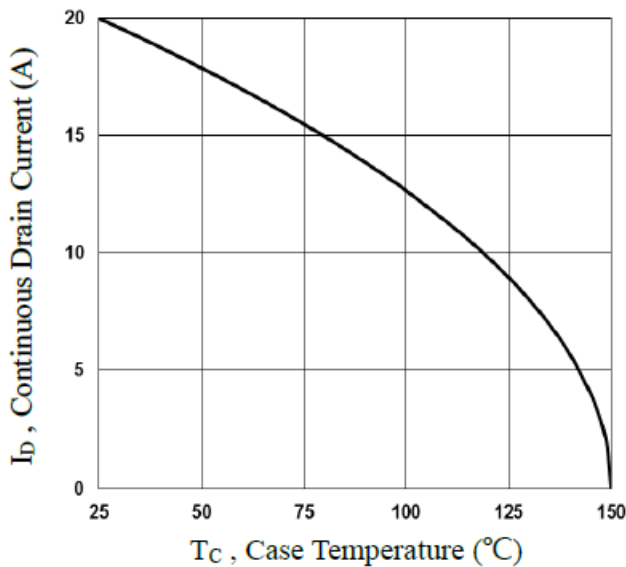


Fig.1 Continuous Drain Current vs. T_C

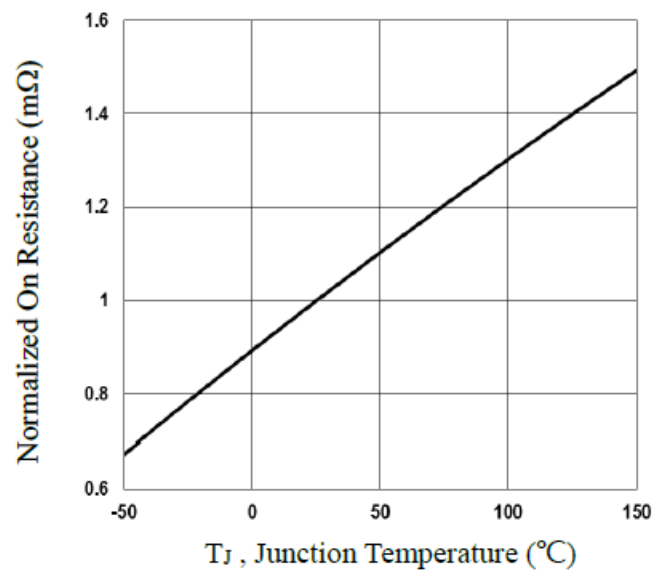


Fig.2 Normalized R_{DS(on)} vs. T_J

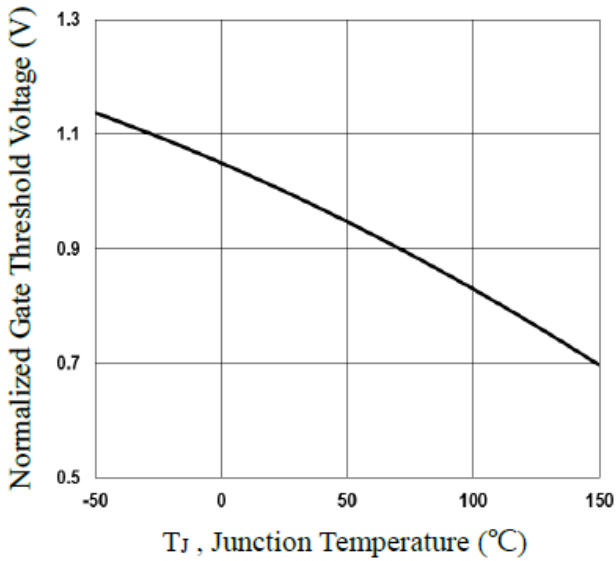


Fig.3 Normalized V_{th} vs. T_j

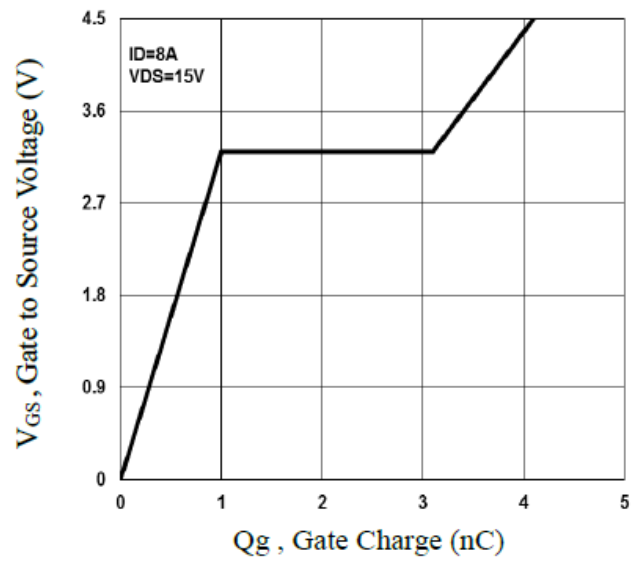


Fig.4 Gate Charge Waveform

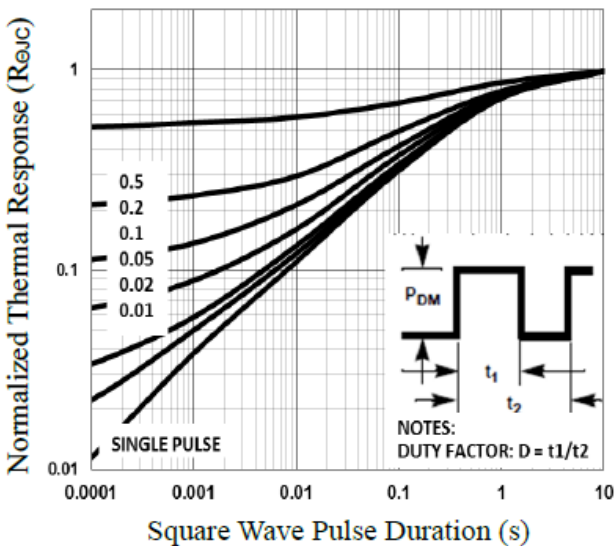


Fig.5 Normalized Transient Response

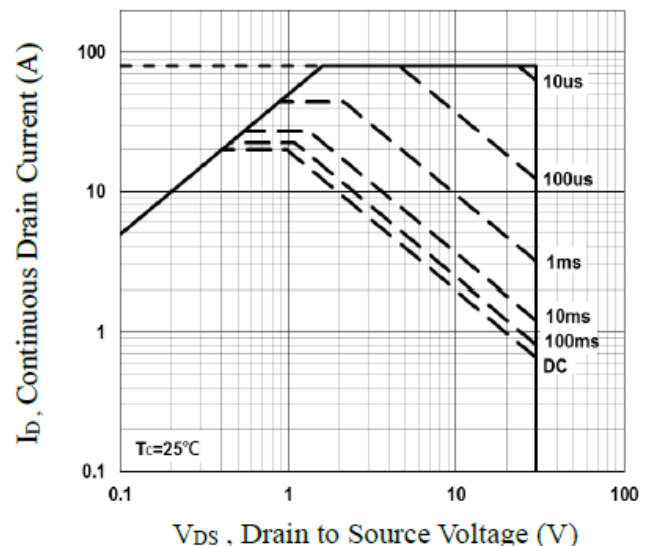


Fig.6 Maximum Safe Operation Area

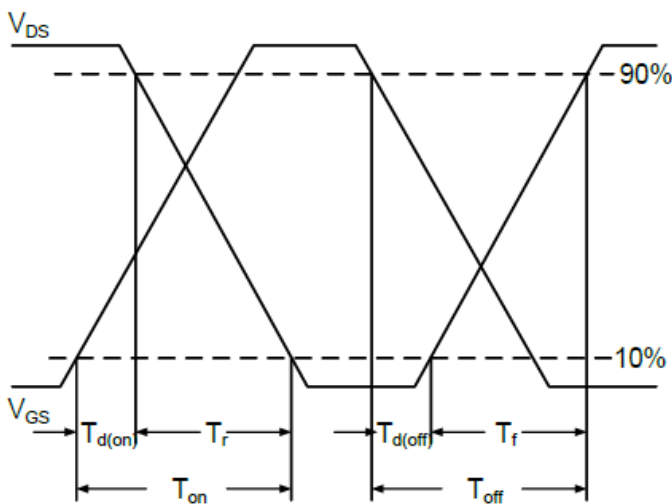


Fig.7 Switching Time Waveform

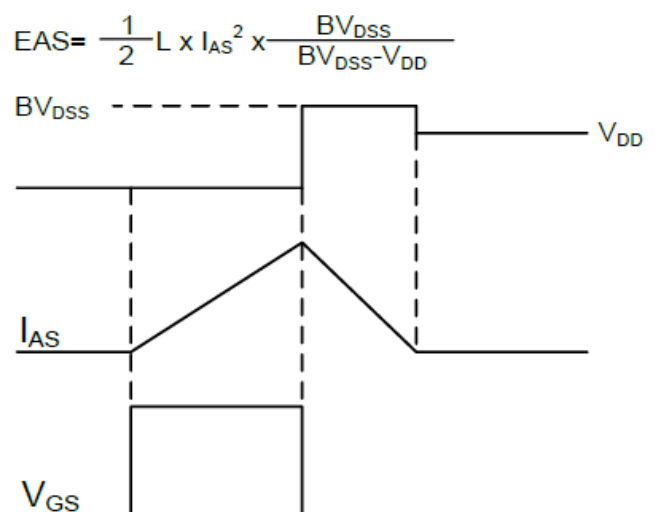
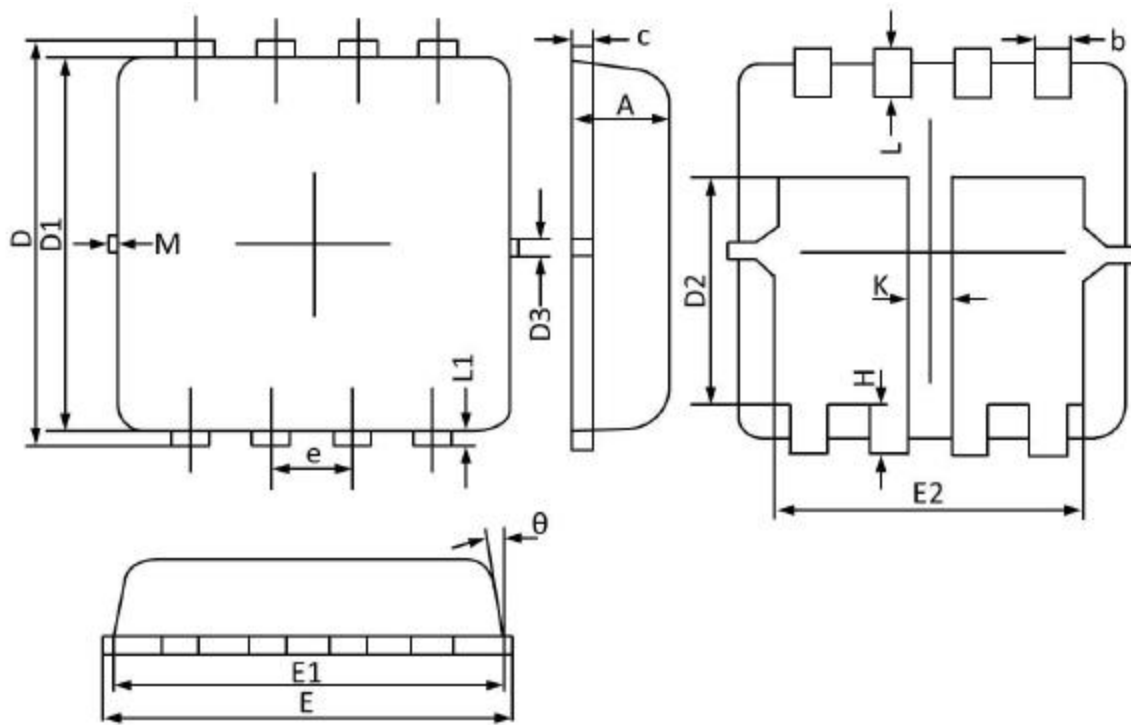


Fig.8 EAS Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

DFN3*3 Dual Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.013
c	0.100	0.250	0.004	0.009
D	3.250	3.450	0.128	0.135
D1	3.000	3.200	0.119	0.125
D2	1.780	1.980	0.070	0.077
D3	0.130 REF		0.005 REF	
E	3.200	3.400	0.126	0.133
E1	3.000	3.200	0.119	0.125
E2	2.390	2.590	0.094	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.011	0.019
L	0.300	0.500	0.011	0.019
L1	0.130 REF		0.005 REF	
K	0.300 REF		0.012 REF	
θ	0°	12°	0°	12°
M	0.150 REF		0.006 REF	