

General Description

EC3618 consists of step-down switching regulator with PWM control. These devise include a reference voltage source, oscillation circuit, error amplifier, internal PMOS and etc. EC3618 provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to the duty ratio linearly form 0 up to 100%. This converter is build out soft start function that prevents overshoot and inrush current at startup. An over current protect function and short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced. An external compensation is easily to system stable; the low ESR output capacitor can be used. With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L with exposed pad package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 58V, it is also suitable for the operation via an AC adapter.

Features

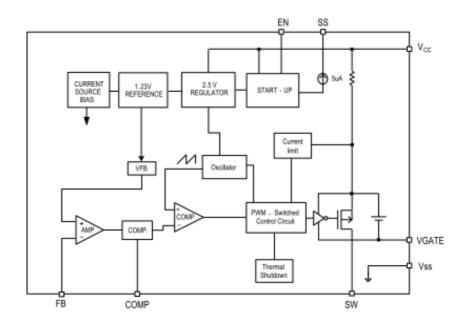
Input voltage: 8V to 58VOutput voltage: 3.3V to 56V

- Duty ratio: 0% to 100% PWM control

- Enable and Soft-Start function

- Current Limit, Short Circuit Protect (SCP) and Thermal Shutdown protection
- Built-in internal SW P-channel MOS.
- SOP-8L-EP Pb-Free package.

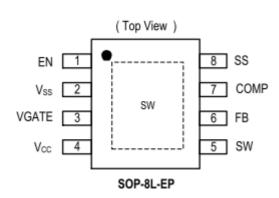
Block Diagram





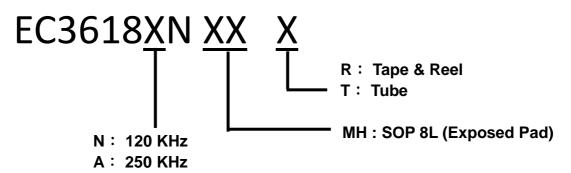
Pin Assignments

The package of EC3618 is SOP-8L-EP; the pin assignment is given by:



Name	Description				
EN	ON/OFF Shutdown pin				
V _{SS}	GND pin				
V _{GATE}	Driver GATE clamping pin. The pin must connect a 0.1uF capacitor to V _{CC}				
V _{cc}	IC power supply pin				
sw	Switch pin. Connect external inductor and diode here.				
FB	Feedback pin				
COMP	Compensation pin				
SS	Soft-start pin				

Ordering Information



Part Number	Package	Marking	Marking Information		
EC3618NNMHR	SOP 8L - (Exposed Pad)	EC3618 LLLLL	 LLLLL: Lot No. YY: Year; Ex: 2012=12 \ 2013=13 WW: Week; 01~52 weeks 		
EC3618ANMHR		YYWWX	3. WW: Week; 01~52 weeks 4. X: N=120KHz; A=250 KHz		



Absolute Maximun Rating (at TA=25℃)

Characteristics		Rating	Unit
V _{CC} Pin Voltage	V_{CC}	V_{SS} - 0.3 to V_{SS} + 63	V
Feedback Pin Voltage	V_{FB}	V _{SS} - 0.3 to 6	V
EN Pin Voltage	V_{EN}	V_{SS} - 0.3 to V_{CC}	V
SS Pin Voltage	SS	V _{SS} - 0.3 to 6	V
COMP Pin Voltage	V_{COMP}	V _{SS} - 0.3 to 6	V
V _{GATE} Pin Voltage	V_{GATE}	V_{SS} - 0.3 to V_{CC}	V
Switch Pin Voltage	V_{SW}	V_{SS} - 0.3 to V_{CC} + 0.3	V
Power Dissipation	PD	Internally limited	mW
Storage Temperature Range	T _{ST}	-65 to +150	\mathcal{C}
Operating Junction Temperature Range	T_{OJP}	-40 to +125	$\mathcal C$
Operating Input Voltage(Note 1)	V _{OP}	8 to 58	V
Thermal Resistance from Junction to case	θ_{JC}	15	€W.
Thermal Resistance from Junction to ambient (Note 2)	θ_{JA}	75	€\M

Note:

- 1. When Vop(Operating Input Voltage)<10V then Maximum output current is 0.45A •
- 2. θ_{JA} is measured with the PCB copper area (need connect to Exposed Pad) of approximately 1.5 in2 (Multi-layer).

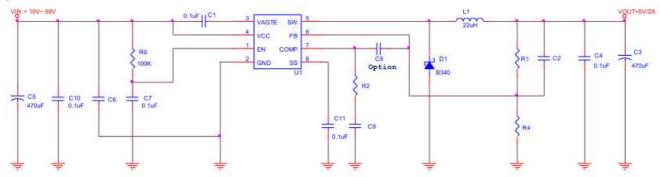
Electrical Characteristics

 $(V_{CC} = 24V, T_A=25C, unless otherwise specified)$

Characteristics	Symbol	Conditions	Min	Тур	Max	Units
Feedback Voltage	VFB	Iout=10mA	0.98	1.00	1.02	V
Quiescent Current	Iccq	VFB=1.2V, force driver off	-	3	6	mA
Feedback Bias Current	lfB	IOUT=0.1A	-	0.1	0.5	uA
Shutdown Supply Current	ISD	VEN =0V	-	0.5	1	mA
Line Regulation	ΔVουτ/Vουτ	VCC =10V~58V, IOUT=10mA	ı	0.3	0.6	%
Load Regulation	Δ V ουτ/V ουτ	IOUT = 0.2 to 2A	-	0.3	0.6	%
Current Limit	ICL		2.8	-	-	Α
Oscillator frequency	Fosc	EC3618N	90	120	150	KHz
Oscillator frequency	Fosc	EC3618A	200	250	300	KHz
	Fosc1	EC3618N ICL	-	50	-	KHz
Short frequency	Fosc1	EC3618A ICL	-	110	-	KHz
Short frequency	Fosc2	EC3618N/EC3618A VFB < 0.3	-	30	-	KHz
EN Pin Logic input threshold	VsH	High (regulator ON)	2.0	-	-	V
voltage	VsL	Low (regulator OFF)	-	-	0.8	
EN Pin Input Current	Ish	VEN=2.5V (ON)	-	5	-	uA
EN FIII IIIput Current	ISL	VEN=0.3V (OFF)	1	5	-	uA
SS Current	Iss	Vss=0V	-	10	-	uA
Internal MOSFET RDSON	RDSON	VCC=12V, 1A,VFB=0V	-	220	300	mΩ
Efficiency (EC3618)	EFFI	VCC = 24V, VOUT = 5V, IOUT = 2A		90	-	%
Thermal shutdown Temp (Junction Temp)	TsD		-	150	-	C
Thermal shutdown Hysteresis	Тѕн		-	40	-	C



Application Circuit



$$V_{OUT} = V_{FB} \times (1 + \frac{R1}{R4})$$

 $V_{FB} = 1.0V$; R4 suggest 0.8K ~ 3K

EL Capacitor Compensation Table						
COUT ESR Range	FREQ	Vout	R2	C9	C2	L1
30m~80mΩ	100KHz	5V	1K	47nF	2200pF	68uH
	250KHz		2.2K	47nF	560pF	33uH
80m~300mΩ	100KHz		1K	47nF	2200pF	68uH
	250KHz		2.2K	47nF	470pF	33uH

Function Descriptions

EN: This pin can be supplied shutdown function. It is inside pull high function. Allow the switching regulator circuit to be shutdown pulling this pin below a 0.8V threshold voltage.

SS: This pin can be supplied soft start function. The pin must be connected a capacitor to ground. There is a 10uA current to charge this capacitor, vary the different capacitor value to control soft start time.

COMP: Compensation pin. For EL output capacitor application, the COMP pin connects R2 and C9 to ground for all condition; please refer the compensation table.

APPLICATION INFORMATION

Setting the Output Voltage

Application circuit item shows the basic application circuit with adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 1.0V \times (1 + \frac{R1}{R4})$$

Table 1 Resistor select for output voltage setting

VOUT	R4	R1	
5V	1.5K	6K	
3.3V	1.3K	3K	



Inductor Selection

For most designs, the different frequency can be reducing the inductor value; The EC3618 is suggested 33μ H. Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 20% of the maximum load current 2A, Δ IL=0.4A. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (2A+0.2A).

Input Capacitor Selection

This capacitor should be located close to the IC using short leads and the voltage rating should be approximately 1.5 times the maximum input voltage. The RMS current rating requirement for the input capacitor of a buck regulator is approximately 1/2 the DC load current. A low ESR input capacitor sized for maximum RMS current must be used. A 470µF low ESR capacitor for most applications is sufficient.

Output Capacitor Selection

The output capacitor is required to filter the output and provide regulator loop stability. The important capacitor parameters are; the 100 KHz Equivalent Series Resistance (ESR), the RMS ripples current rating, voltage rating, and capacitance value. For the output capacitor, the ESR value is the most important parameter. The ESR can be calculated from the following formula.

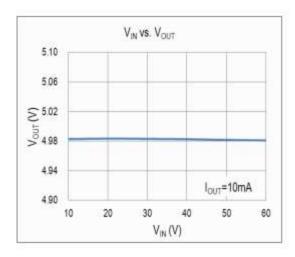
$$V_{RIPPLE} = \Delta I_L \times ESR = 0.4$$
A x 80m Ω = 32mV

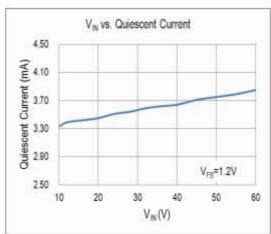
An aluminum electrolytic capacitor's ESR value is related to the capacitance and its voltage rating. In most case, higher voltage electrolytic capacitors have lower ESR values. Most of the time, capacitors with much higher voltage ratings may be needed to provide the low ESR values required for low output ripple voltage. It is recommended to replace this low ESR capacitor by using a $470\mu F$ low ESR values $< 80m\Omega$.

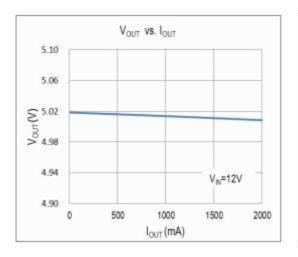


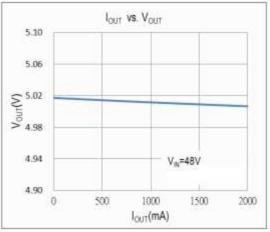


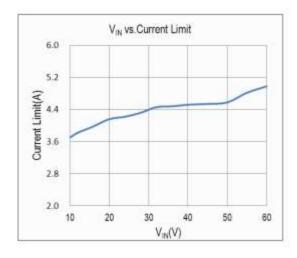
Typical Characteristics





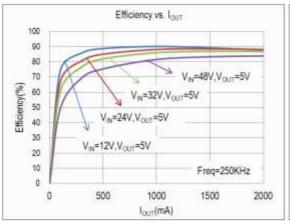


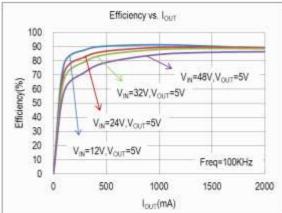


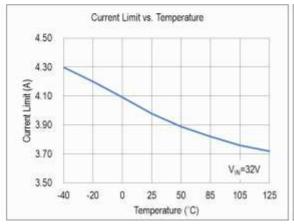


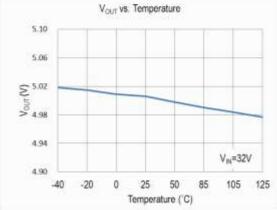


Typical Characteristics (Continuous)



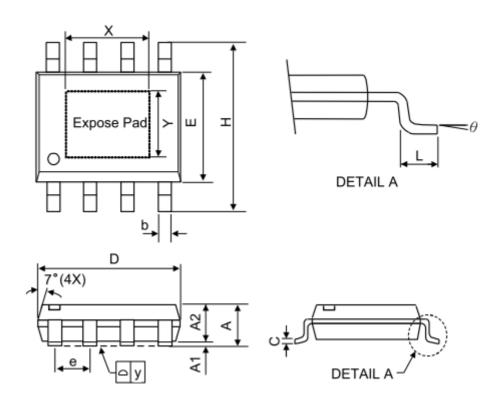








Package Outlines



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
Α	-	-	1.75	-	-	0.069
A1	0	-	0.15	0	-	0.06
A2	1.25	-	-	0.049	-	-
С	0.1	0.2	0.25	0.0075	0.008	0.01
D	4.7	4.9	5.1	0.185	0.193	0.2
Е	3.7	3.9	4.1	0.146	0.154	0.161
Н	5.8	6	6.2	0.228	0.236	0.244
L	0.4	-	1.27	0.015	-	0.05
b	0.31	0.41	0.51	0.012	0.016	0.02
е	1.27 BSC			0	.050 BSC	
у	-	-	0.1	-	-	0.004
Χ	-	2.34	-	-	0.092	-
Υ	-	2.34	-	-	0.092	-
θ	00	-	80	00	-	80

Mold flash shall not exceed 0.25mm per side

JEDEC outline: MS-012 BA