

## General Description

The EC5911 is wideband, low-noise, low-distortion operational amplifier, that offer rail-to-rail output and single-supply operation down to 2.5V. They draw 2.8mA of quiescent supply current, as well as low input voltage-noise density (13nV/ Hz) and low input current-noise density (400fA/ Hz). These features make the devices an ideal choice for applications that require low distortion and low noise. The EC5911 has output which swing rail-to-rail and their input common-mode voltage range includes ground and offer wide bandwidth to 200MHz (G=+1). They are specified over the extended industrial temperature range (-45 C ~ 125 C). The single EC5911 is available in space-saving, SOT23-5 and SOP-8 packages.

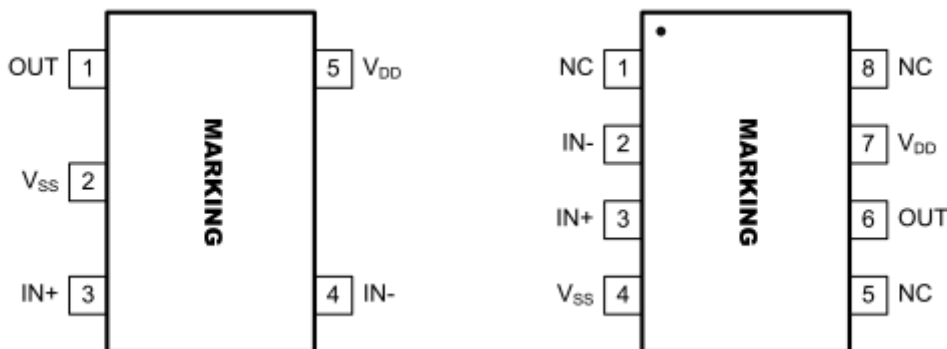
## Features

- Single-Supply Operation from +2.5V ~ +5.5V
- Rail-to-Rail Input / Output
- Gain-Bandwidth Product: 200MHz (Typ.)
- Low Input Bias Current: 10pA (Typ.)
- Low Offset Voltage: 5mV (Max.)
- Quiescent Current: 2.8mA (Typ.)
- Operating Temperature: -40°C ~ +125°C
- Available in SOT23-5 and SOP8 Packages

## Applications

- Portable Equipment
- Mobile Communications
- Smoke Detector
- Sensor interface
- Medical Instrumentation
- Handheld Test Equipment
- imaging / video

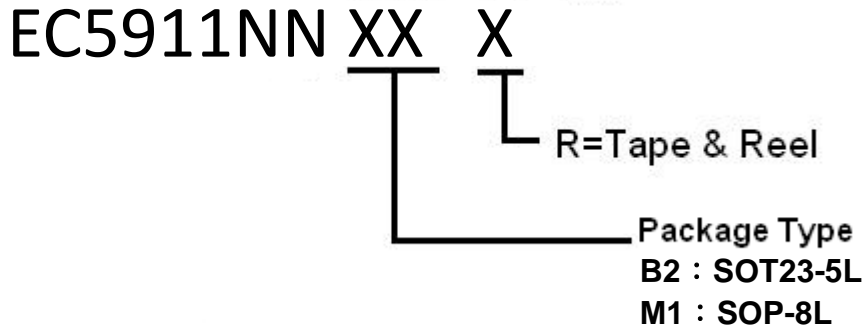
## Pin Assignments



**Figure 1. Pin Assignment Diagram (SOT23-5 and SOP8 Package)**



## Ordering Information



Part Number	Package	Marking	Marking Information
EC5911NNB2R	SOT23-5L	911YW	1. Y : Year code(D=2013;E=2014;F=2015...) 2. W : Week Code( The big character of A~Z is for the week of 1~26, and small a~z is for the week of 27~52.
EC5911NNM1R	SOP-8L	EC5911 LLLLL YYWWT	1. LLLLL : Last five Number of Lot No 2. YY : Year Code 3. WW : Week Code 4. T : Internal Tracking Code

## Absolute Maximum Ratings

Condition	Min	Max
Power Supply Voltage ( $V_{DD}$ to $V_{SS}$ )	-0.5V	+7V
Analog Input Voltage ( $IN+$ or $IN-$ )	$V_{SS}-0.5V$	$V_{DD}+0.5V$
PDB Input Voltage	$V_{SS}-0.5V$	+7V
Operating Temperature Range	-40°C	+125°C
Junction Temperature	+150°C	
Storage Temperature Range	-65°C	+150°C
Lead Temperature (soldering, 10sec)	+300°C	
Package Thermal Resistance ( $T_A=+25^\circ\text{C}$ )		
SOT23-5, $\theta_{JA}$	190°C	
SOP8, $\theta_{JA}$	130°C	

**Note:** Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.



**Electrical Characteristic**

( $V_{DD} = +5V$ ,  $V_{SS} = 0V$ ,  $V_{CM} = 0V$ ,  $V_{OUT} = V_{DD}/2$ ,  $R_L = 100K$  tied to  $V_{DD}/2$ ,  $SHDNB = V_{DD}$ ,  $T_A = -40^\circ C$  to  $+125^\circ C$ , unless otherwise noted. Typical values are at  $T_A = +25^\circ C$ .) (Notes 1,2)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Supply-Voltage Range	$V_{DD}$	Guaranteed by the PSRR test	2.5	-	5.5	V
Quiescent Supply Current (per Amplifier)		$V_{DD} = 5V$	-	2.8	3.3	mA
Input Offset Voltage	$V_{OS}$	$T_A = 25^\circ C$	-	$\pm 1$	-	mV
		$T_A = -40^\circ C \sim +85^\circ C$	-	$\pm 8$	-	
		$T_A = -40^\circ C \sim +125^\circ C$	-	-	$\pm 10$	
Input Offset Voltage Tempco	$\Delta V_{OS}/\Delta T$		-	$\pm 2$	-	$\mu V/^\circ C$
Input Bias Current	$I_B$	(Note 3)	-	$\pm 10$	$\pm 100$	pA
Input Offset Current	$I_{OS}$	(Note 3)	-	$\pm 10$	$\pm 100$	pA
Input Common-Mode Voltage Range	$V_{CM}$	Guaranteed by the $T_A = 25^\circ C$ CMRR test, $T_A = -40^\circ C \sim +125^\circ C$	-0.1	-	$V_{DD} + 0.1.5$	V
Common-Mode Rejection Ratio	CMRR	$V_{SS} - 0.1V \leq V_{CM} \leq V_{DD} + 0.1V$ $T_A = 25^\circ C$	-	75	-	dB
		$V_{SS} \leq V_{CM} \leq 5V_{DD}$ $T_A = 25^\circ C$	72	90	-	
		$V_{SS} - 0.1V \leq V_{CM} \leq V_{DD} + 0.1V$ $T_A = -40^\circ C \sim +125^\circ C$	-	68	-	
Power-Supply Rejection Ratio	PSRR	$V_{DD} = +2.5V$ to $+5.5V$	75	90	-	dB
Open-Loop Voltage Gain	$A_V$	$R_L = 10k\Omega$ to $V_{DD}/2$ $V_{OUT} = 100mV$ to $V_{DD} - 125mV$	90	100	-	dB
		$R_L = 1k\Omega$ to $V_{DD}/2$ $V_{OUT} = 200mV$ to $V_{DD} - 250mV$	80	95	-	
		$R_L = 500\Omega$ to $V_{DD}/2$ $V_{OUT} = 350mV$ to $V_{DD} - 500mV$	70	80	-	
Output Voltage Swing	$V_{OUT}$	$ V_{IN+} - V_{IN-}  \geq 10mV$ $V_{DD} - V_{OH}$	-	10	30	mV
		$R_L = 10k\Omega$ to $V_{DD}/2$ $V_{OL} - V_{SS}$	-	10	35	
		$ V_{IN+} - V_{IN-}  \geq 10mV$ $V_{DD} - V_{OH}$	-	80	50	
		$R_L = 1k\Omega$ to $V_{DD}/2$ $V_{OL} - V_{SS}$	-	30	50	
		$ V_{IN+} - V_{IN-}  \geq 10mV$ $V_{DD} - V_{OH}$	-	100	140	
		$R_L = 500\Omega$ to $V_{DD}/2$ $V_{OL} - V_{SS}$	-	100	140	
Output Short-Circuit Current	$I_{SC}$	Sinking or Sourcing	-	$\pm 100$	-	mA
Gain Bandwidth Product	GBW	$A_V = +1V/V$	-	200	-	MHz
Slew Rate	SR	$A_V = +1V/V$	-	125	-	V/ $\mu s$



## Electrical Characteristic

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Differential Phase error (NTSC)	DP	$G=2, R_L=150\Omega$	-	0.03	-	deg
Differential Gain error (NTSC)	DG	$G=2, R_L=150\Omega$	-	0.09	-	dB
Settling Time	$t_s$	To 0.01%, $V_{OUT} = 2V$ step $A_v = +1V/V$	-	52	-	$\mu s$
Over Load Recovery Time		$V_{IN} \times Gain = V_s$	-	2	-	$\mu s$
Input Voltage Noise Density	$e_n$	$f = 1kHz$	-	15	-	nV/ $\sqrt{Hz}$
		$f = 30kHz$	-	13	-	
Input Current Noise Density	$i_n$	$f = 1kHz$	-	400	-	fA/ $\sqrt{Hz}$
Total Harmonic Distortion plus Noise	THD+N	$f_C=5MHz, V_{OUT}=2V_{p-p}, G=+2$	-	-60	-	dB

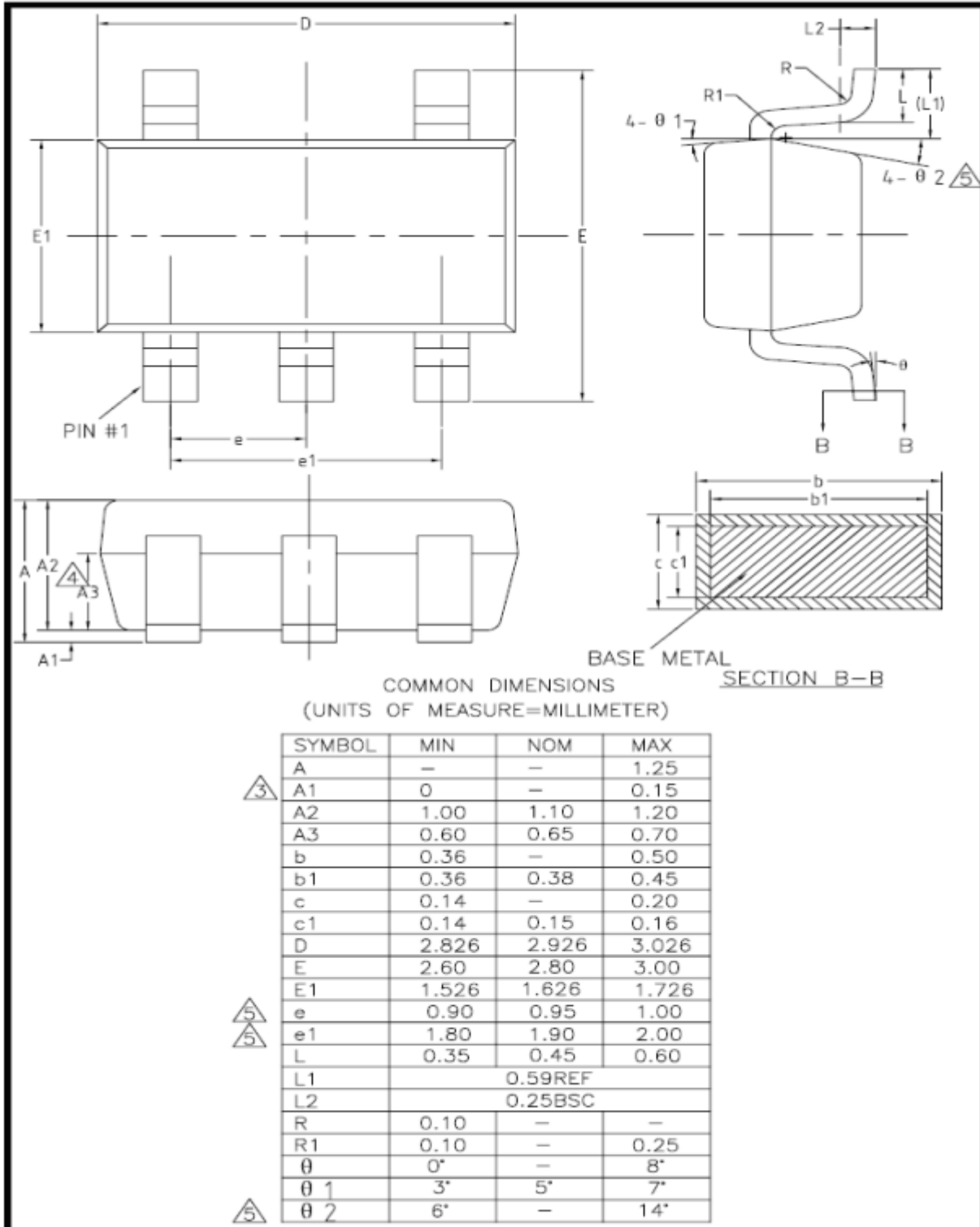
Note 1: All devices are 100% production tested at  $T_A = +25^\circ C$ ; all specifications over the automotive temperature range is guaranteed by design, not production tested.

Note 2: Parameter is guaranteed by design.

Note 3: Peak-to-peak input noise voltage is defined as six times rms value of input noise voltage.

Package Information

SOP23-5



SOP8

