

Description

The ENP2305 uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications.

General Features

- VDS =-20V , ID =-4.2A
RDS(ON)(Typ.) =43mΩ @VGS=-2.5V
RDS(ON)(Typ.) =31mΩ @VGS=-4.5V
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

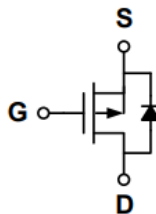
Application

- PWM applications
- Load switch

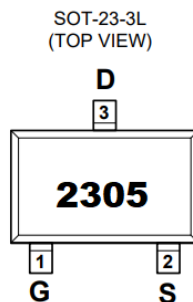
Package

- SOT-23-3L

Schematic diagram



Marking and pin assignment



Ordering Information

ENP2305 XX GR

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B1= SOT-23-3L

**Absolute Maximum Ratings** (TA=25°C unless otherwise noted)

parameter	symbol	limit	unit
Drain-source voltage	V _{DS}	-20	V
Gate-source voltage	V _{GS}	±12	V
Drain current-continuous ^a @Tj=125°C -pulse ^b	I _D	-4.2	A
	I _{DM}	-13	A
Drain-source Diode forward current	I _S	-1.25	A
Maximum power dissipation	P _D	1.2	W
Operating junction Temperature range	T _J	-55—150	°C

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF Characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-20	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-	-1	μA
Gate-body leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
ON Characteristics						
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-0.5	-0.85	-1.5	V
Drain-source on-state resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-4.2A	-	31	45	mΩ
		V _{GS} =-2.5V, I _D =-3A	-	41	60	
Forward transconductance	g _{fs}	V _{GS} =-5V, I _D =-2A	-	5	-	S
Dynamic Characteristics						
Input capacitance	C _{ISS}	V _{DS} =-10V, V _{GS} =0V f=1.0MHz	-	740	-	pF
Output capacitance	C _{OSS}		-	290	-	
Reverse transfer capacitance	C _{RSS}		-	190	-	
Switching Characteristics						
Turn-on delay time	t _{D(ON)}	V _{DD} =-10V I _D =-2.8A V _{GEN} =-4.5V R _L =10ohm R _{GEN} =-60ohm	-	12.5	-	ns
Rise time	t _r		-	35	-	
Turn-off delay time	t _{D(OFF)}		-	30	-	
Fall time	t _f		-	10	-	
Total gate charge	Q _g	V _{DS} =-10V, I _D =-3A V _{GS} =-4.5V	-	6.1	-	nC
Gate-source charge	Q _{gs}		-	1.7	-	
Gate-drain charge	Q _{gd}		-	1.2	-	
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode forward voltage	V _{SD}	V _{GS} =0V, I _S =-1.25A	-	-0.81	-1.2	V

Notes:

- surface mounted on FR4 board, t≤10sec
- pulse test: pulse width≤300μs, duty≤2%
- guaranteed by design, not subject to production testing

Thermal Characteristics

Thermal Resistance junction-to ambient	R _{th JA}	100	°C/W
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Typical Performance Characteristics

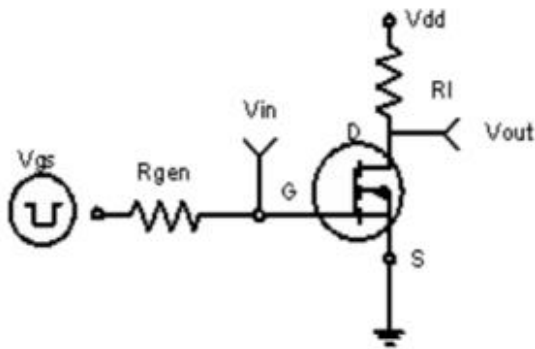


Figure 1: Switching Test Circuit

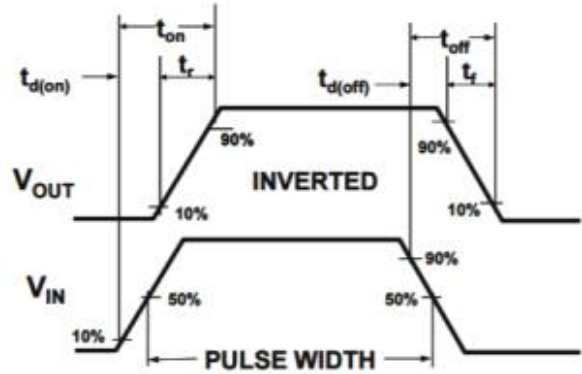


Figure 2: Switching Waveforms

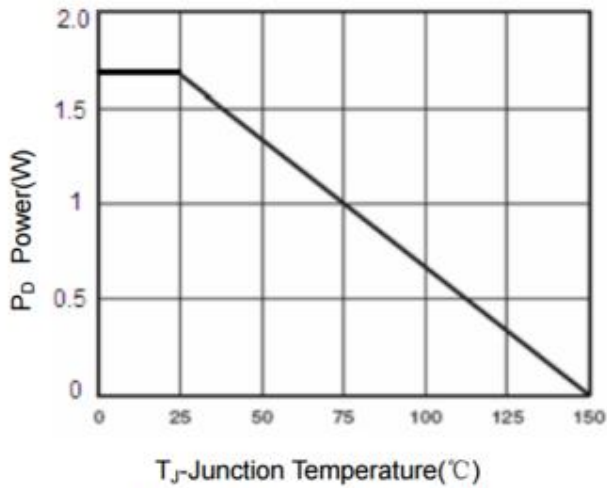


Figure 3 Power Dissipation

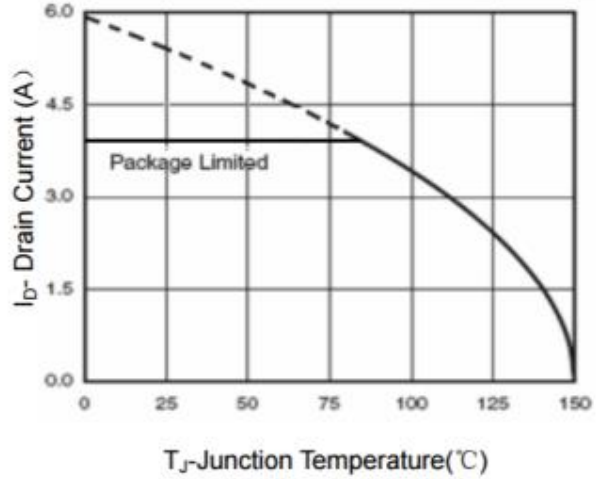


Figure 4 Drain Current

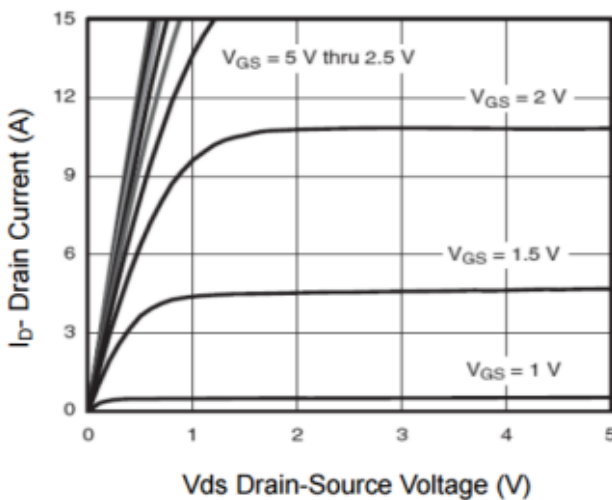


Figure 5 Output Characteristics

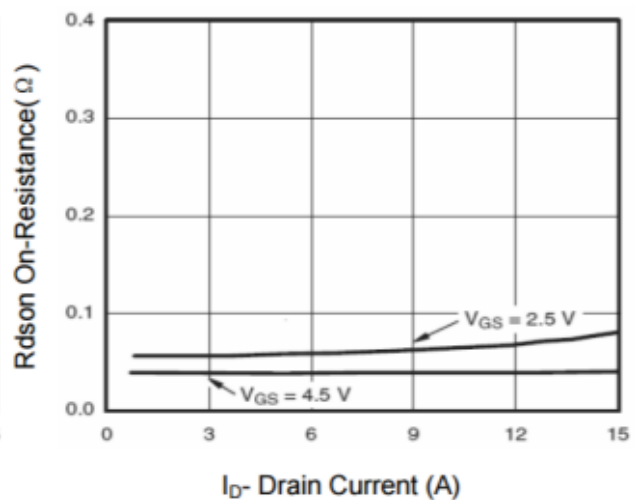


Figure 6 Drain-Source On-Resistance

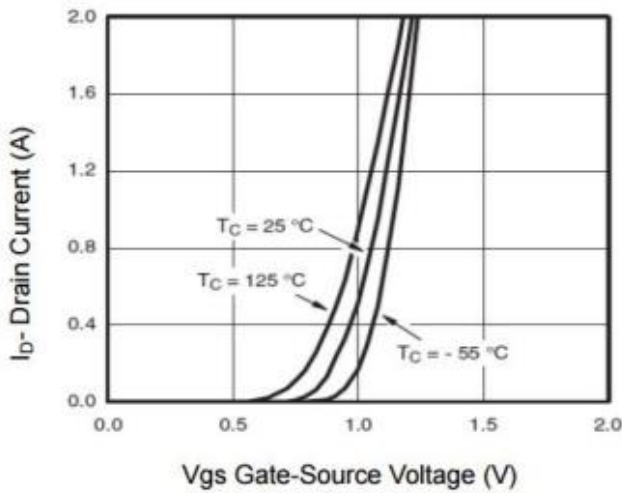


Figure 7 Transfer Characteristics

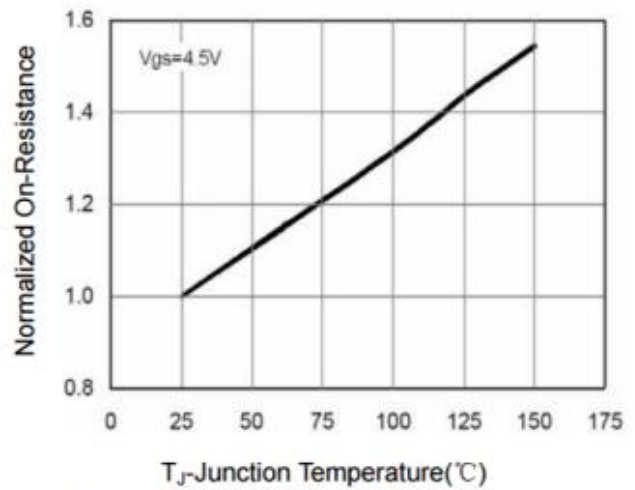


Figure 8 Drain-Source On-Resistance

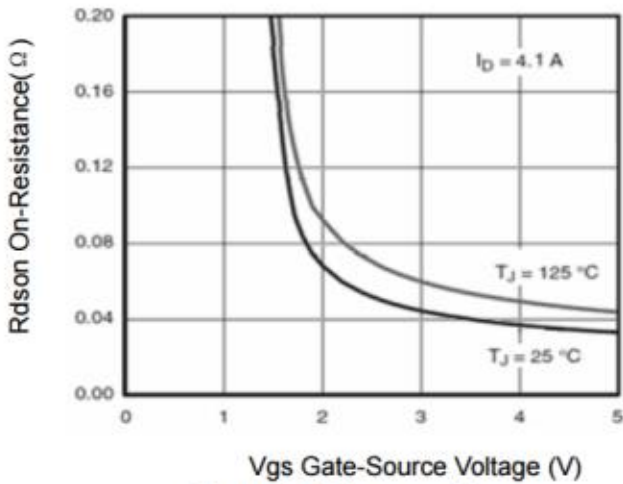


Figure 9 Rdson vs Vgs

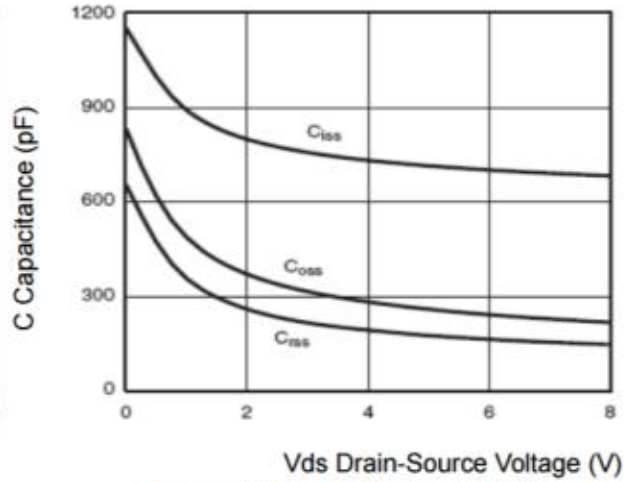


Figure 10 Capacitance vs Vds

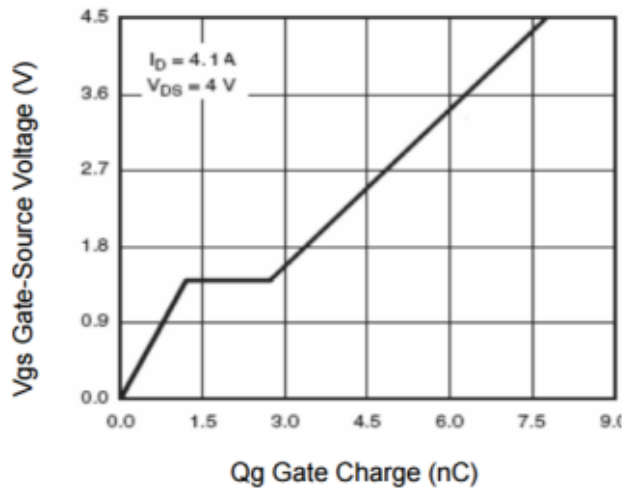


Figure 11 Gate Charge

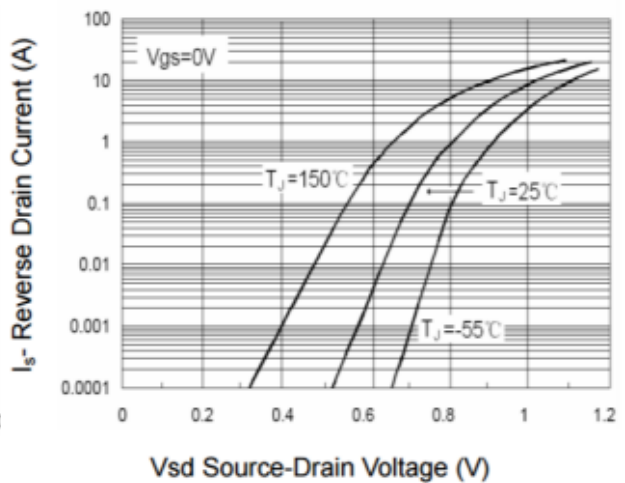


Figure 12 Source- Drain Diode Forward

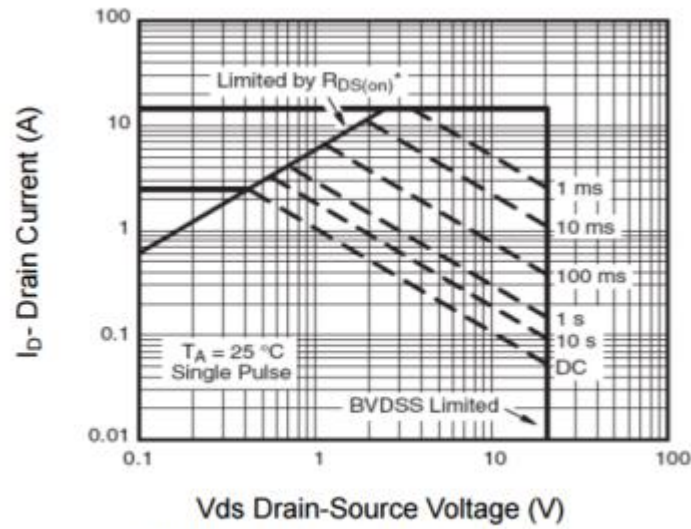


Figure 13 Safe Operation Area

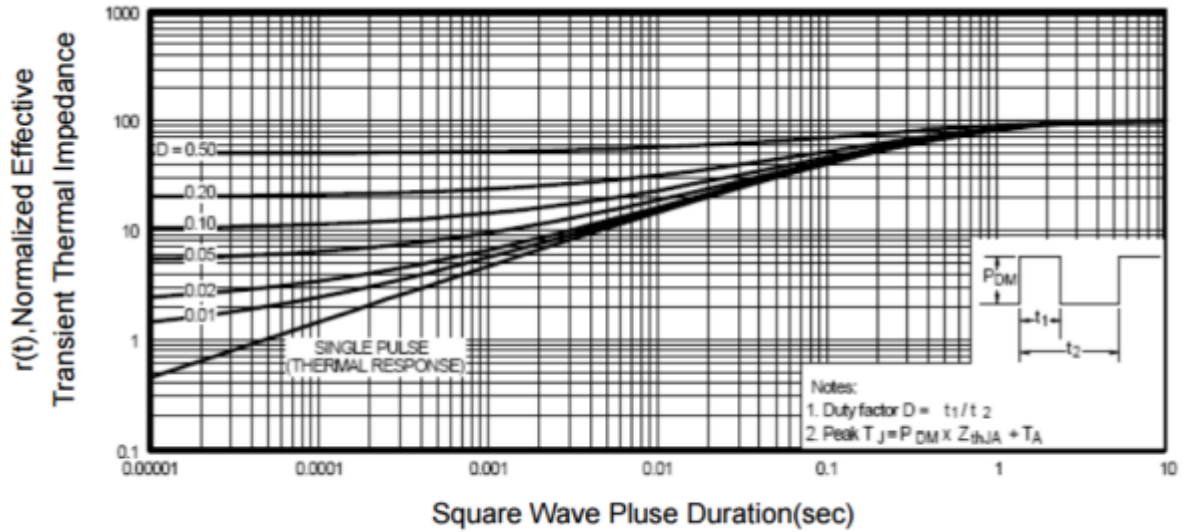
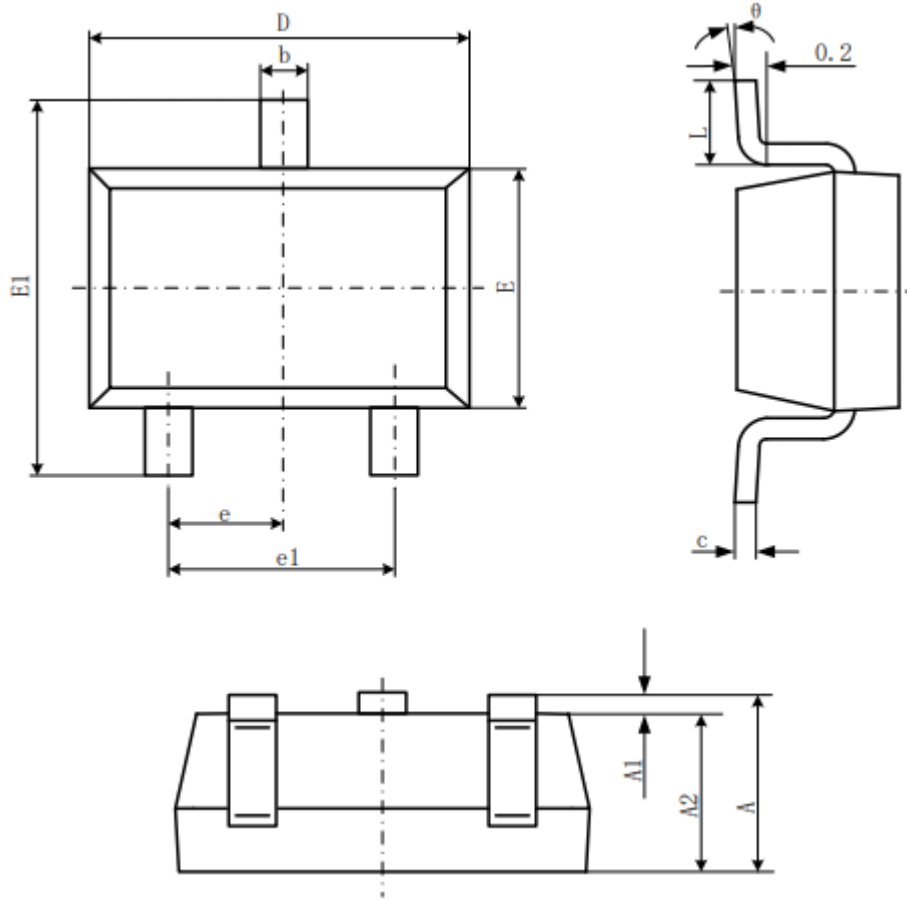


Figure 14 Normalized Maximum Transient Thermal Impedance

Package Information

- SOT-23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°